

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**  
Patent Application

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Filed: 02/03/04      Examiner:

Serial No.:

Title: METHOD FOR GENERATING A DEEP N-WELL PATTERN FOR AN INTEGRATED CIRCUIT DESIGN

Form 1449

**U.S. Patent Documents**

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
<i>CB</i>	A	6,489,224	12/03/02	Burr	438	526	05/31/01
	B	6,218,708	04/17/01	Burr	257	372	02/25/98
	C	6,087,892	07/11/00	Burr	327	534	06/08/98
	D	6,303,444	10/16/01	Burr	438	289	10/19/00
	E	6,048,746	04/11/00	Burr	438	17	06/08/98
<i>CB</i>	F	6,091,283	07/18/00	Murgula et al.	327	537	02/24/98

**Foreign Patent or Published Foreign Patent Application**

Examiner Initial	No.	Document No.	Publication Date	Country or Patent Office	Class	Sub-class	Translation	
							Yes	No
	G							
	H							
	I							

**Other Documents**

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
	J	
	K	
	L	
Examiner	<i>Chywh</i>	
	Date Considered	<i>5/8/2006</i>

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.